



FOR IMMEDIATE RELEASE

SystemCrafter Joins Xilinx ESL Initiative

Ipswich, England. March 13, 2006. SystemCrafter announced participation in the ESL Initiative (www.xilinx.com/esl) launched today by Xilinx, Inc, the worldwide leader of programmable logic solutions. This collaboration will help SystemCrafter to drive integration and adoption of SystemC to VHDL software as part of the ESL methodology for FPGAs, and enable the company to more rapidly develop enhanced versions in the future.

Working at the Electronic System-Level (ESL) is set to revolutionize the design of hardware and systems. Not only does it allow designers to work at a higher level of abstraction, improving time to market and reliability, but it also enables hardware to be designed by programmers and scientists. Combining ESL software with FPGAs enables designers to go from a conceptual C++ project to working hardware in hours.

“There is a growing interest in ESL design among FPGA designers,” said Steve Lass, director of Software Product Marketing at Xilinx. “The aim of our initiative is to improve awareness among the FPGA designer community, and to help our ecosystem partners expand the market for their solutions. We are working closely with SystemCrafter to enhance the value of their SystemC-to-VHDL software for programmable hardware platforms by improving the quality of results and integration with Xilinx software tools.”

SystemCrafter’s software, which won an EE Times Ultimate Product award and has been nominated for a 2006 EE Times ACE award, automatically synthesises hardware designs written in SystemC to VHDL. The VHDL can then be used with Xilinx FPGA design tools to target Xilinx FPGAs. This enables engineers and programmers to design, debug and simulate hardware and systems using their existing C++ development environment.

Dr Jonathan Saul, SystemCrafter CEO, commented, “We are delighted to be working with Xilinx as part of their ESL initiative. Our vision is to provide a tool that can bridge the gap between SystemC and FPGAs, at an affordable price. This enables system-level designers to implement their designs quickly and reliably, and opens up the use of FPGAs to a new market of C and C++ programmers.”

The price of SystemCrafter SC is only \$2995 for a perpetual node-locked licence. The package can be purchased direct from the web at www.systemcrafter.com. A free evaluation version can be downloaded and used for 30 days.

About SystemC

SystemC is a worldwide standard for modelling hardware and software systems using the C/C++ language with a library for hardware constructs. As well as allowing hardware and software to be simulated in the same framework, it is also more compact than VHDL or Verilog. It is faster to write, and more maintainable and

readable, and can be compiled into an executable specification for fast simulation.
www.systemc.org

About SystemCrafter

SystemCrafter exists to make it easier, faster and less risky to create advanced IC designs. To fulfill this mission, they have created the award-winning SystemCrafter SC software tool. SystemCrafter SC is an affordable tool that automatically synthesizes designs written in the industry-standard SystemC language to electronic hardware. For more information visit www.systemcrafter.com.

About Xilinx ESL Initiative

The Xilinx ESL Initiative is a multi-faceted program aimed at proliferating high-level design methodologies and tool flows for FPGAs. The goal is to make it easier for hardware designers and software programmers to leverage Xilinx programmable devices for their next generation systems. Xilinx and participating companies are focused on improving ease of use, quality of results and interoperability standards through technical collaboration, cooperative marketing and joint educational activities. For more information about the Xilinx ESL Initiative, visit www.xilinx.com/esl.

All trademarks are acknowledged.

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